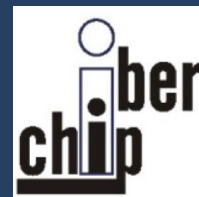


IBERCHIP 2020

26th Iberchip Workshop
San José, Costa Rica
February 26-28, 2020



Call for Papers

<http://www.ieee-lascas.org/>



The Iberchip workshop provides an annual forum to academic and industrial researchers from Iberoamerican countries in which to exchange experiences, share knowledge, and establish relations to foster the development of activities related to the field of microelectronics. Special emphasis is put in the improvement of education and training, and in the promotion of joint cooperative projects.

The 26th edition will take place in San José, the capital of Costa Rica. The city is located in the central region of the country, a strategic place nearby the Juan Santamaría International Airport (SJO), where major government institutions, public universities, and industry converge in a metropolitan area with over two million people. The west urban area of San José hosts a vibrant business environment, encompassing the operation of more than 200 high-tech companies in fields such as electronics, software, and biomedical applications.

Topics of Interest:

- Analog and Digital Circuits
- Mixed-Mode Circuits
- Biomedical Electronics
- Communication/RF Circuits
- Wireless Sensor Networks
- Nanoelectronics
- Electronic Design Automation
- VLSI Systems and Applications
- FPGA Design and Applications
- Circuit Testing
- Fault Tolerant Circuits
- Electronics, systems and circuits for space and nuclear applications
- Other topics related to Microelectronics

IBERCHIP is technically co-sponsored by IEEE CASS. Papers must not exceed four pages in length and must be written using the IEEE template.

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Paper Submission:

September 30th, 2019

Notification of acceptance:

November 11th, 2019.

Camera-ready papers:

November 25th, 2019.